晶采光電科技股份有限公司
AMPIRE CO., LTD.

SPECIFICATIONS FOR
LCD MODULE

CUSTOMER	
CUSTOMER PART NO.	
AMPIRE PART NO.	**AG-320240A4FICW-TG1(N)(R)**
APPROVED BY	
DATE	

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RECORD OF REVISION

Revision Date	Page	Contents	Editor
2006/11/16	-	New Release	Tony
2007/3/21	3	Modify Features & Mechanical Data.	Edward
	4	Modify Optical Characteristics	Edward
	6	Add Touch Panel Electrical Specification	Edward
	7	Modify Block Diagram	Edward
	8	Modify Pin Connections	Edward
	9-10	Modify Timing Characteristics	Edward
	16	Modify Outline Dimension	Edward

1 FEATURES

(1) Display format : 320 × 240 dot-matrix, 1/240 duty.

(2) Construction : FSTN LCD, Bezel, Heat Seal, Zebra, CCFL back-light and PCB, Touch panel.

(3) Display Type: FSTN, Transflective, Positive type, 6 o'clock view.

(4) White CCFL back-light.

(5) Controller : RA8835.

(6) Besides +5.0V/+3.3V for logic circuit, -20V is needed for LCD driving

(7) Normal temperature type.

(8) ROHS compliant.

2 MECHANICAL DATA

Parameter	Stand Value	Unit
Dot size	0.345(W) × 0.345(H)	mm
Dot pitch	0.36(W) × 0.36(H)	mm
Viewing area	122.0(W) × 92.0(H)	mm
Module size (with LED)	167.1(W) × 109.0(H) × 12.5 max (T)	mm

3 ABSOLUTE MAXIMUM RATINGS

Parameter		Symbol	Min	Max	Unit
Logic Circuit Supply Voltage		VDD-VSS	-0.3	7.0	V
LCD Driving Voltage		VDD-VO	-0.3	26.0	V
Input Voltage		VI	-0.3	VDD+0.3	V
Normal temp. type	Operating Temp.	TOP	0	50	°C
	Storage Temp.	TSTG	-20	70	°C

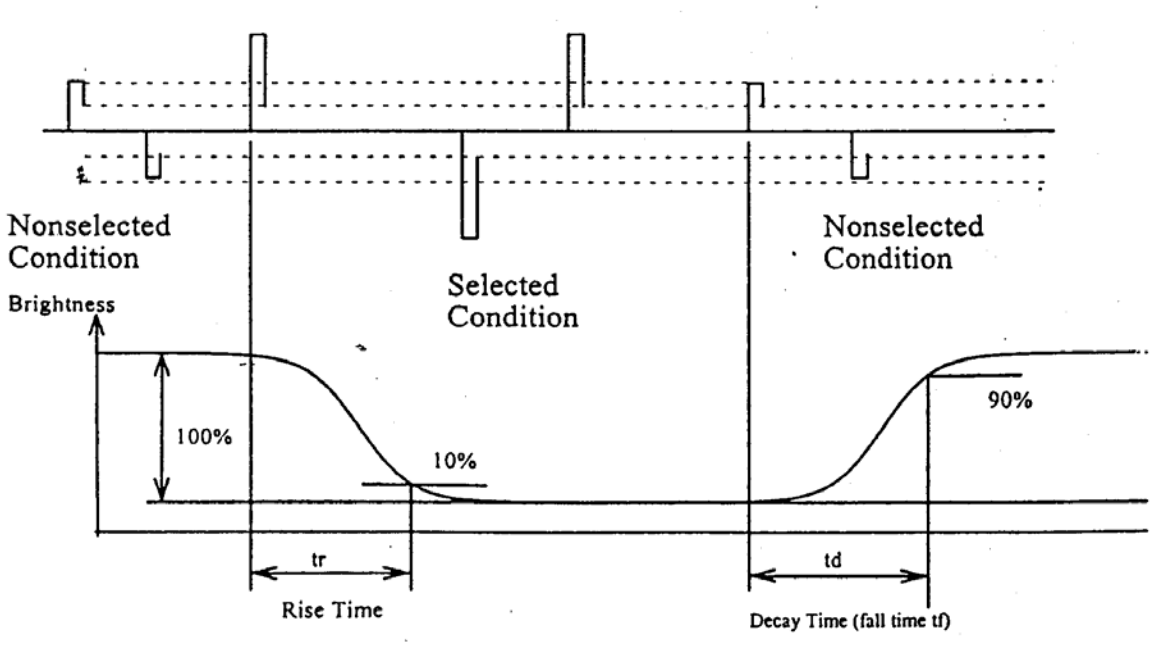
4 ELECTRO-OPTICAL CHARACTERISTICS

Parameter	Symbol	Condition	Min	Typ	Max	Unit	Note
----- Electronic Characteristics -----							
Logic Circuit Supply Voltage	VDD-VSS	--	3.3	--	5.0	V	
LCD Driving Voltage	VDD-VO	0 °C	21.2	22.4	23.5		
		25 °C	20.5	21.6	22.7		
		50 °C	19.6	20.8	21.9		
Input Voltage	VIH	--	0.7 VDD	--	VDD	V	
	VIL	--	VSS	--	0.3 VDD	V	
Logic Supply Current	IDD	VDD = 5V	--	30	--	mA	
----- Optical Characteristics -----							
Contrast	CR	FSTN type	--	5	--		Note 1
Rise Time	tr	25°C	--	200	300	ms	Note 2
Fall Time	tf	25°C	--	200	300	ms	
Viewing Angle Range	θ f	25°C & CR≥2	--	40	--	Deg.	Note 3
	θ b		--	35	--		
	θ l		--	35	--		
	θ r		--	35	--		
Frame Frequency	fF	25°C	--	64	--	Hz	

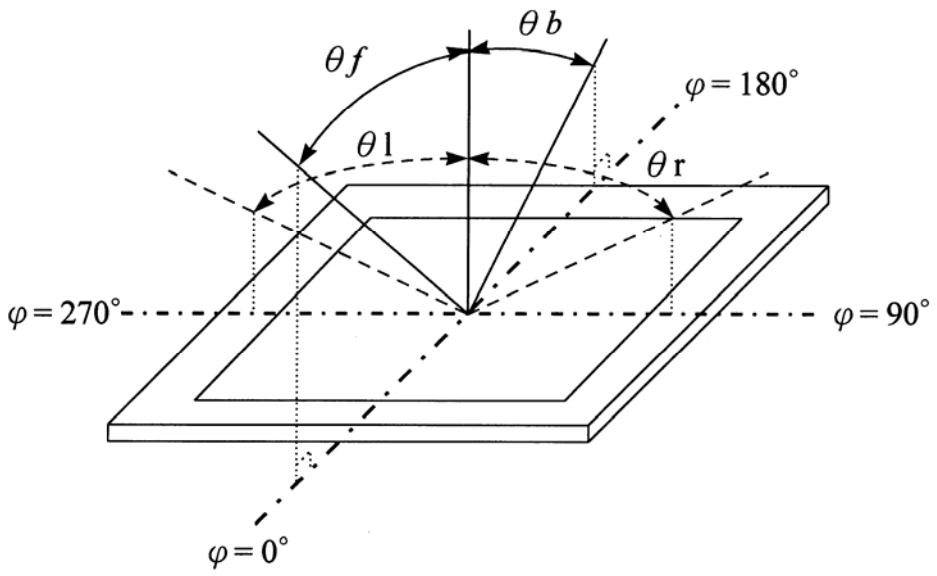
(NOTE 1) Contrast ratio :

CR = (Brightness in OFF state) / (Brightness in ON state)

(NOTE 2) Response time :



(NOTE 3) Viewing angle



4.1 CCFL Back-light Electrical Specification

Parameter	Condition	Standard Value	Unit
Tube Voltage	Ta=25 °C	370	Vrms
Tube Current	Ta=25 °C	5	mArms
LCM brightness	--	150	Cd / m^2
Half-Brightness Life*	--	10,000	hour
Life Under Low Temperature	Ta=0 °C	Above 400	hour

*The life-time of the average brightness reach to 50% of initial brightness .

4.2 Touch Panel Electrical Specification

Parameter	Specification	Condition
ON Resistance	300 Ω ~ 900 Ω	X Axis
	200 Ω ~ 650 Ω	Y Axis
Insulating Resistance	More than 20MΩ	DC 25 V
Linearity	--	±1.5 %
Notes life by Pen	Note a	100,000 times(min)
Input life by finger	Note b	1,000,000 times (min)

Note A .

Notes area for pen notes life test is 10 x 9 mm.

Size of word is 7.5 x 6.72

Shape of pen end : R0.8

Load : 250 g

Note B

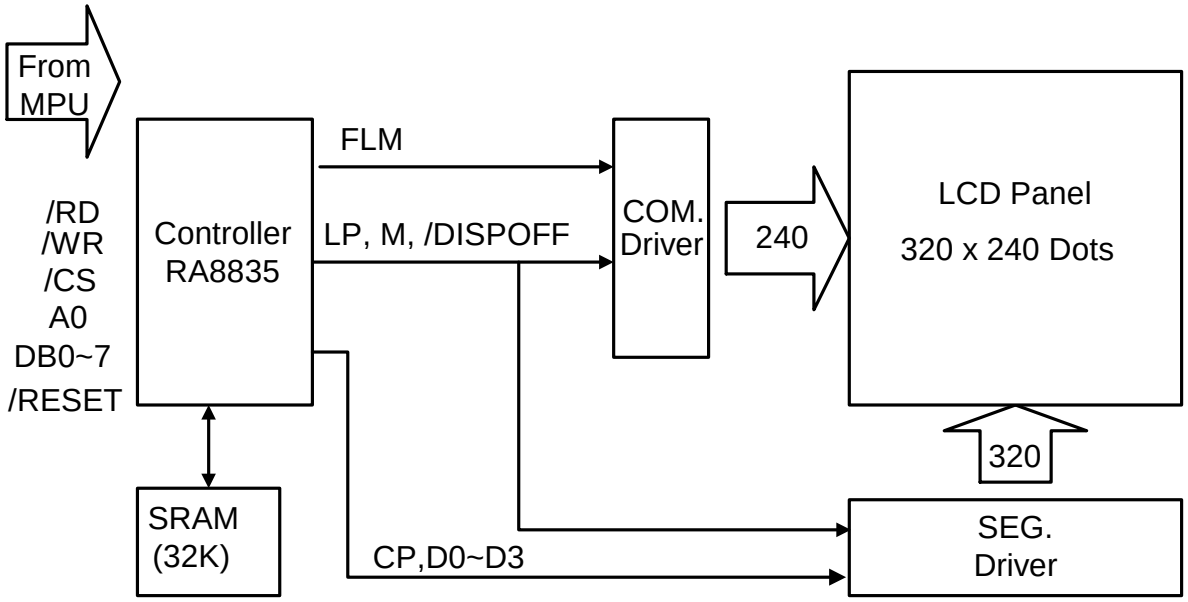
By Silicon rubber tapping at same point

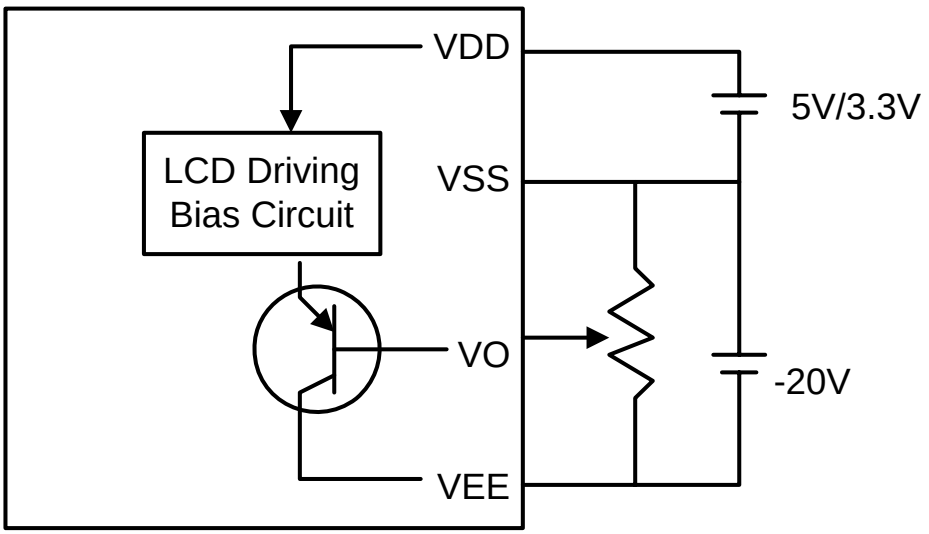
Shape of rubber end : R8

Load : 200g

Frequency : 5 Hz

5 BLOCK DIAGRAM & POWER SUPPLY





6 PIN CONNECTIONS

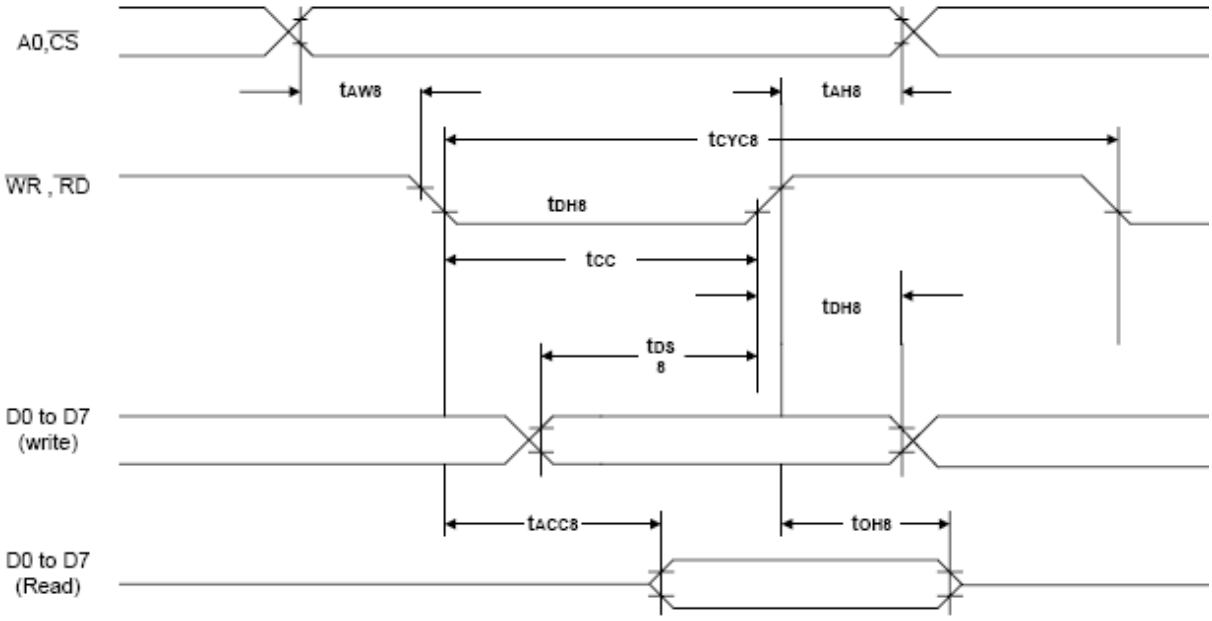
NO.	SIGNAL	LEVEL	FUNCTION
1	/RESET	H/L	Reset Signal
2	/RD	H/L	80 Series: Read Signal 68 Series: Enable Signal(E)
3	/WR	H/L	80 Series: Write Signal 68 Series: R/W Signal
4	/CS	H/L	Chip Select Signal
5	A0	H/L	Data Type Selection
6 ~ 13	DB0~DB7	H/L	Data Input(8 bits)
14	VDD	-	Power Supply for Logic(+5.0V)
15	VSS	-	Power Supply(Ground : 0V)
16	VEE	-	Negative voltage input (-20V)
17	VO	-	Contrast Adjustment Input
18*	SK / X1	-	Serial Clock Touch Panel Left Signal in X Axis
19*	DO / X2	-	Data Output Touch Panel Right Signal in X Axis
20*	DI / Y1	-	Data In Touch Panel Upper Signal in Y Axis
21*	CS / Y2	-	T/P controller Chip Select. Active Low Touch Panel Lower Signal in X Axis
22*	INT	-	Interrupt
23~24	NC	-	No connection

* 18~22 : SK, DO, DI, CS, INT for Touch Panel controller TSC2046

/ X1, X2, Y1, Y2 for Touch Panel (without TSC2046)

7 TIMING CHARACTERISTICS

7.1 8080 Family Interface Timing



Ta = –20 to 75°C

Signal	Symbol	Parameter	V_{DD} = 4.5 to 5.5V		V_{DD} = 2.7 to 4.5V		Unit	Condition
			Min.	Max.	Min.	Max.		
A0, $\overline{CS}$	t_{AH8}	Address hold time	10	—	10	—	ns	CL = 100pF
	t_{AW8}	Address setup time	0	—	0	—	ns	
$\overline{WR}$, $\overline{RD}$	t_{CYC8}	System cycle time	note.	—	note.	—	ns	
	t_{CC}	Strobe pulse width	$20+t_C$	—	$50+t_C$	—	ns	
D0 to D7	t_{DS8}	Data setup time	120	—	120	—	ns	
	t_{DH8}	Data hold time	5	—	5	—	ns	
	t_{ACC8}	$\overline{RD}$ access time	—	50	—	80	ns	
	t_{OH8}	Output disable time	10	50	10	55	ns	

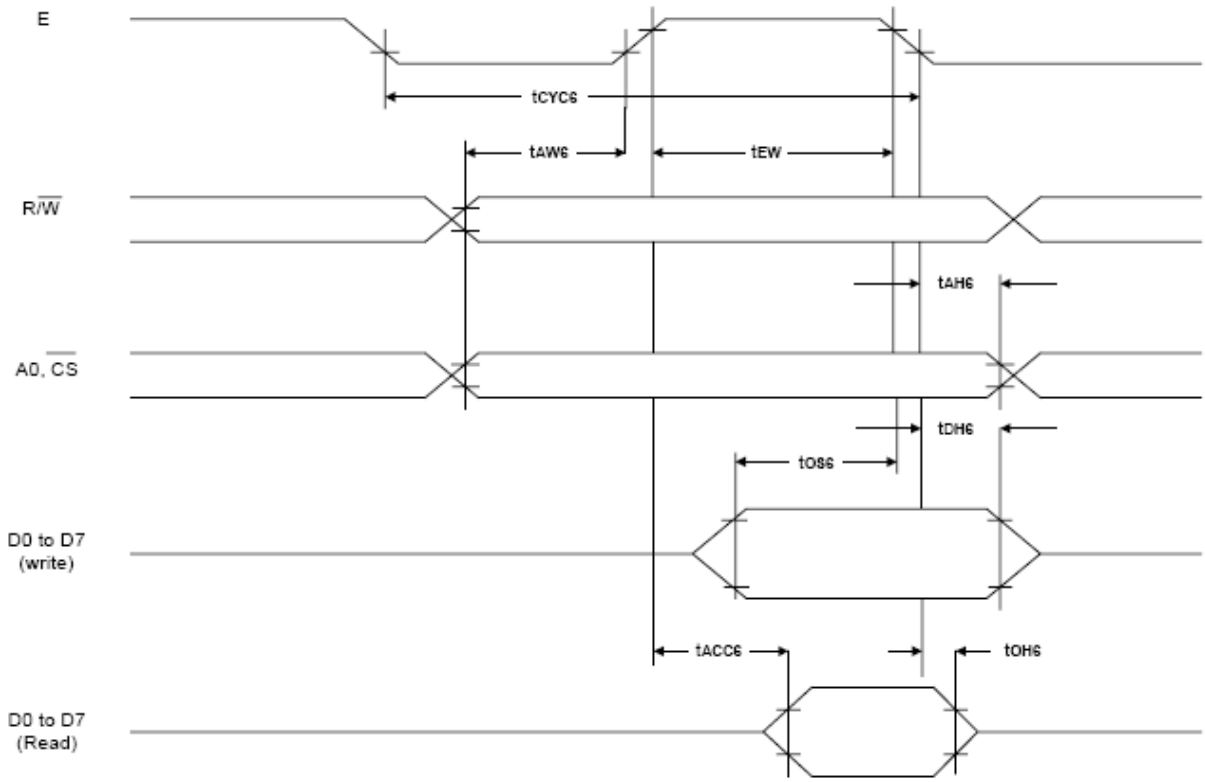
Note: For memory control and system control commands:

$t_{CYC8} = 2t_C + t_{CC} + t_{CEA} + 75 > t_{ACV} + 245$

For all other commands:

$t_{CYC8} = 4t_C + t_{CC} + 30$

7.2 6800 Family Interface Timing



Ta = –20 to 75°C

Signal	Symbol	Parameter	V_{DD} = 4.5 to 5.5V		V_{DD} = 2.7 to 4.5V		Unit	Condition
			Min.	Max.	Min.	Max.		
A0, $\overline{CS}$, R/($\overline{W}$)	t_{CYC6}	System cycle time	note.	—	note.	—	ns	CL = 100 pF
	t_{AW6}	Address setup time	0	—	10	—	ns	
	t_{AH6}	Address hold time	0	—	0	—	ns	
D0 to D7	t_{DS6}	Data setup time	100	—	120	—	ns	
	t_{DH6}	Data hold time	0	—	0	—	ns	
	t_{OH6}	Output disable time	10	50	10	75	ns	
	t_{ACC6}	Access time	—	85	—	130	ns	
E	t_{EW}	Enable pulse width	$20+t_C$	—	$50+t_C$	—	ns	

Note: For memory control and system control commands:

$t_{CYC6} = 2t_C + t_{EW} + t_{CEA} + 75 > t_{ACV} + 245$

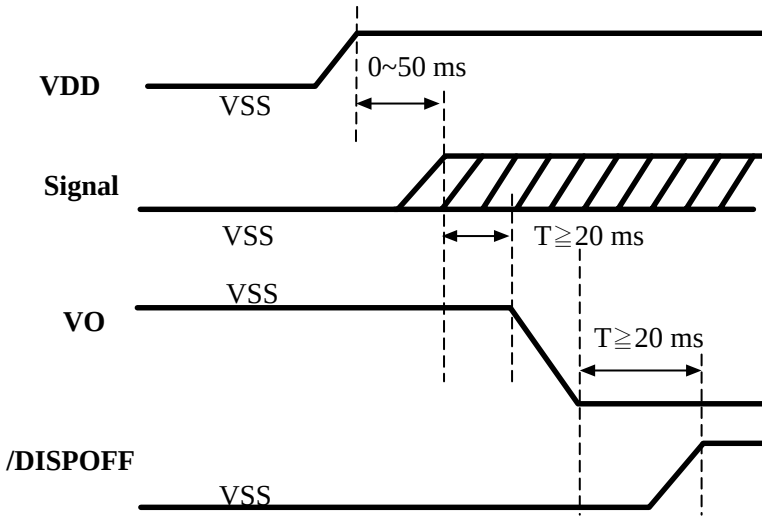
For all other commands:

$t_{CYC6} = 4t_C + t_{EW} + 30$

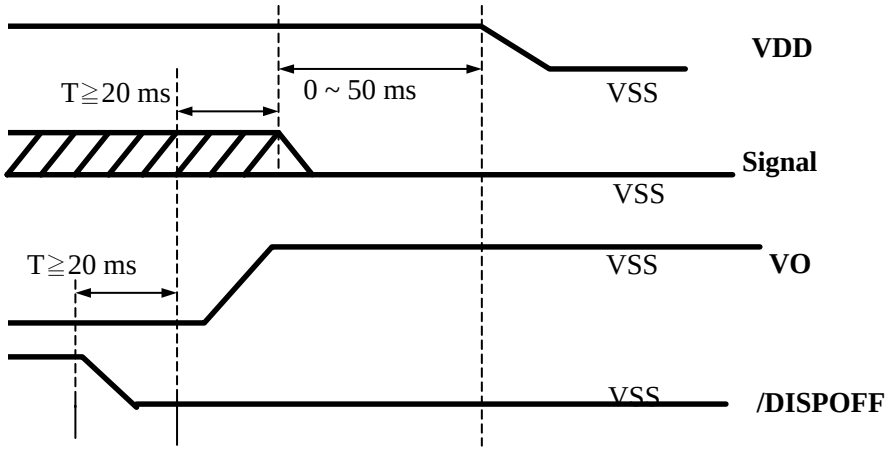
7.3 Power ON/OFF Sequence

Please maintain the blow sequence when turning on and off the power supply of the module. If /DISPOFF is supplied to the module while internal alter signal for LCD driving (M) is unstable, DC component will be supplied to the LCD panel. This may cause damage the LCD module.

POWER ON SEQUENCE



POWER OFF SEQUENCE



8 QUALITY AND RELIABILITY

8.1 TEST CONDITIONS

Tests should be conducted under the following conditions :

Ambient temperature : 25 ± 5°C

Humidity : 60 ± 25% RH.

8.2 SAMPLING PLAN

Sampling method shall be in accordance with MIL-STD-105E , level II, normal single sampling plan .

8.3 ACCEPTABLE QUALITY LEVEL

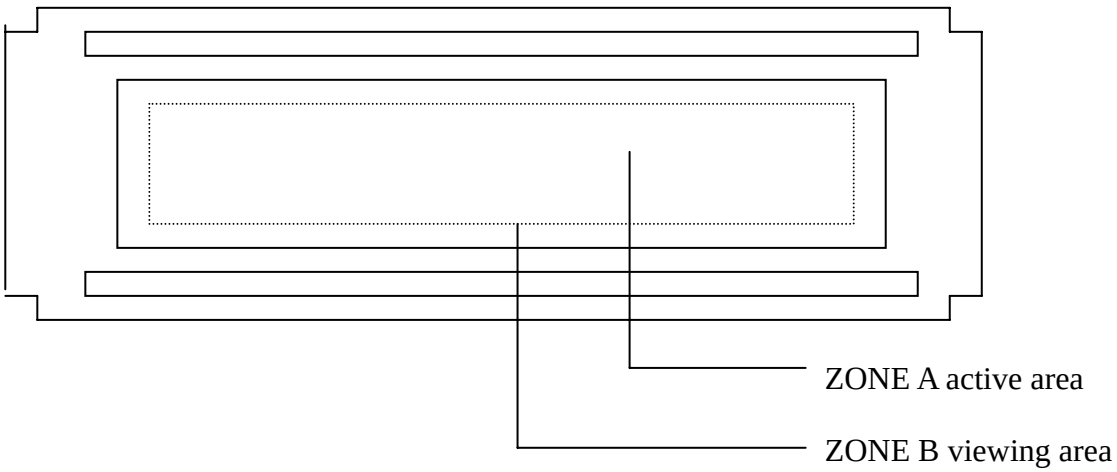
A major defect is defined as one that could cause failure to or materially reduce the usability of the unit for its intended purpose. A minor defect is one that does not materially reduce the usability of the unit for its intended purpose or is an infringement from established standards and has no significant bearing on its effective use or operation.

8.4 APPEARANCE

An appearance test should be conducted by human sight at approximately 30 cm distance from the LCD module under flourescent light. The inspection area of LCD panel shall be within the range of following limits.

8.5 INSPECTION QUALITY CRITERIA

Item	Description of defects			Class of Defects	Acceptable level (%)
Function	Short circuit or Pattern cut			Major	0.65
Dimension	Deviation from drawings			Major	1.5
Black spots	Ave . dia . D	area A	area B	Minor	2.5
	D≤0.1	Disregard			
	0.1<D≤0.2	3	5		
	0.2<D≤0.25	2	3		
	0.25<D≤0.30	0	1		
	0.3<D	0	0		
Black lines	Width W, Length L	A	B	Minor	2.5
	W≤0.03	disregard			
	0.03<W≤0.05	3	4		
	0.05<W≤0.07 , L≤3.0	1	1		
	See line criteria				
Bubbles in polarizer	Average diameter D 0.2 < D < 0.5 mm for N = 4 , D > 0.5 for N = 1			Minor	2.5
Color uniformity	Rainbow color or newton ring.			Minor	2.5
Glass Scratches	Obvious visible damage.			Minor	2.5
Contrast ratio	See note 1			Minor	2.5
Response time	See note 2			Minor	2.5
Viewing angle	See note 3			Minor	2.5



8.6 RELIABILITY

Test Item	Test Conditions		Note
	Normal Temp. type	Extended Temp. type	
High Temperature Operation	50±3°C , t=96 hrs	70±3°C , t=96 hrs	
Low Temperature Operation	0±3°C , t=96 hrs	-20±3°C , t=96 hrs	
High Temperature Storage	70±3°C , t=96 hrs	80±3°C , t=96 hrs	1,2
Low Temperature Storage	-20±3°C , t=96 hrs	-30±3°C , t=96 hrs	1,2
Temperature Cycle	-20°C ~ 25°C ~ 70°C 30 min. 5 min. 30 min. (1 cycle) Total 5 cycle	-30°C ~ 25°C ~ 80°C 30 min. 5 min. 30 min. (1 cycle) Total 5 cycle	1,2
Humidity Test	40 °C, Humidity 90%, 96 hrs		1,2
Vibration Test (Packing)	Sweep frequency : 10 ~ 55 ~ 10 Hz/1min Amplitude : 0.75mm Test direction : X.Y.Z/3 axis Duration : 30min/each axis		2

Note 1 : Condensation of water is not permitted on the module.

Note 2 : The module should be inspected after 1 hour storage in normal conditions (15-35°C , 45-65%RH).

Definitions of life end point :

- Current drain should be smaller than the specific value.
- Function of the module should be maintained.
- Appearance and display quality should not have degraded noticeably.
- Contrast ratio should be greater than 50% of the initial value.

9 HANDLING PRECAUTIONS

(1) A LCD module is a fragile item and should not be subjected to strong mechanical shocks.

(2) Avoid applying pressure to the module surface. This will distort the glass and cause a change in color.

(3) Under no circumstances should the position of the bezel tabs or their shape be modified.

(4) Do not modify the display PCB in either shape or positioning of components.

(5) Do not modify or move location of the zebra or heat seal connectors.

(6) The device should only be soldered to during interfacing. Modification to other areas of the board should not be carried out.

(7) In the event of LCD breakage and resultant leakage of fluid do not inhale, ingest or make contact with the skin. If contact is made rinse immediately.

(8) When cleaning the module use a soft damp cloth with a mild solvent, such as Isopropyl or Ethyl alcohol. The use of water, ketone or aromatic is not permitted.

(9) Prior to initial power up input signals should not be applied.

(10) Protect the module against static electricity and observe appropriate anti-static precautions.

10 OUTLINE DIMENSION